

FIG. 1

112
114
116
118

MISMATCH CALCULATOR FOR DATA ENTRY

☐ smos7lv
☐ MOS
☐ nlv
☐ Voltage Driven

120
122

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Model= smos7lv_sps_tsg.udlib.rev0b

Calculate
Reset
email address: [xxxxx]

	Single	String	-OR-	Range	From	To	Steps
Vd: (V)	[xxx]	[xxxxx]			[xxx]	[xxx]	[xxx]
Vg: (V)	[xxx]	[xxxxx]			[xxx]	[xxx]	[xxx]
Vb: (V)	[xxx]	[xxxxx]			[xxx]	[xxx]	[xxx]
W: (cdr)	[xxx]	[xxxxx]			[xxx]	[xxx]	[xxx]
L: (cdr)	[xxx]	[xxxxx]			[xxx]	[xxx]	[xxx]
Temp (C):	[xxx]	[xxxxx]			[xxx]	[xxx]	[xxx]

Cross Coupled? ☐ Center-to-Center [xxx] Sigmas(#) [xxx]

☒ Show Mismatch Process Parameter Contributions
☐ Show Capacitances and Conductances
☐ Show Vdsat at [xx] dB drop from peak Rout: Step Size= [xx] V

MISMATCH RESULTS

W= 2	Vd= 2.5	X-Cpl= OFF
L= 2	Vg= 1	CTC= 0.0000
	Vb= 0	Sigma= 1
		Temp= 27

	Id MM (+/-)	Vg MM (+/-)
Total	2.7323%	4.9071mV
dl	0.0294	0.0442
dwox	0.0091	0.0193
gox	0.2070	0.4081
nsub	0.0118	0.0236
rsh	0.0000	0.0000
ubref	0.3572	0.7159
vfb	2.6715	4.7872
vtl	0.0026	0.0044
vtw	0.3956	0.6924

MISMATCH NOTES

Mismatch model based on Lot MB19168, wafer7 from C1SD

FIG. 2

007050 664560

Model= smos7lv_sps_tsg.udlib.rev0b

Calculate

Reset

email address:

122

124

126

128

138

	Single	-OR-	String	-OR-	Range	
					From	To
Vd: (V)	<input type="text" value="xxx"/>		<input type="text" value="xxxxx"/>		<input type="text" value="xxx"/>	<input type="text" value="xxx"/>
Vg: (V)	<input type="text" value="xxx"/>		<input type="text" value="xxxxx"/>		<input type="text" value="xxx"/>	<input type="text" value="xxx"/>
Vb: (V)	<input type="text" value="xxx"/>		<input type="text" value="xxxxx"/>		<input type="text" value="xxx"/>	<input type="text" value="xxx"/>
W: (cdr)	<input type="text" value="xxx"/>		<input type="text" value="xxxxx"/>		<input type="text" value="xxx"/>	<input type="text" value="xxx"/>
L: (cdr)	<input type="text" value="xxx"/>		<input type="text" value="xxxxx"/>		<input type="text" value="xxx"/>	<input type="text" value="xxx"/>
Temp (C):	<input type="text" value="xxx"/>		<input type="text" value="xxxxx"/>		<input type="text" value="xxx"/>	<input type="text" value="xxx"/>

134

Cross Coupled? ☐ Center-to-Center Sigmas(#)

136

☒ Show Mismatch Process Parameter Contributions
☐ Show Capacitances and Conductances
☐ Show Vdsat at dB drop from peak Rout: Step Size= V

152a

MISMATCH RESULTS		
W= 2	Vd= 2.5	X-Cpl= OFF
L= 2	Vg= 1	CTC= 0.0000
	Vb= 0	Sigma= 1
		Temp= 27

154a

	Id MM (+/-)	Vg MM (+/-)
157a Total	2.7323%	4.9071mV
dl	0.0294	0.0442
dwox	0.0091	0.0193
gox	0.2070	0.4081
nsub	0.0118	0.0236
rsh	0.0000	0.0000
ubref	0.3572	0.7159
vfb	2.6715	4.7872
vtl	0.0026	0.0044
vtw	0.3956	0.6924

155a

159a

FIG. 4

150a

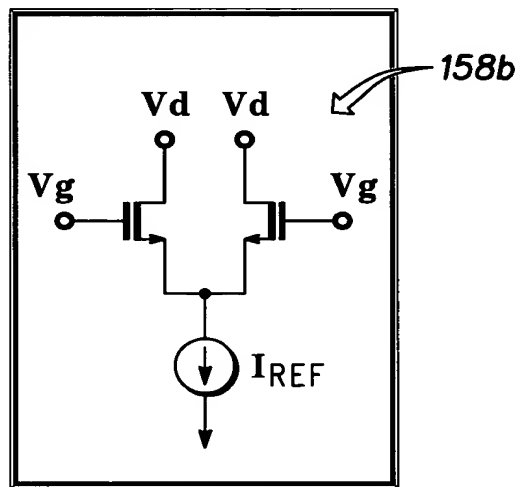
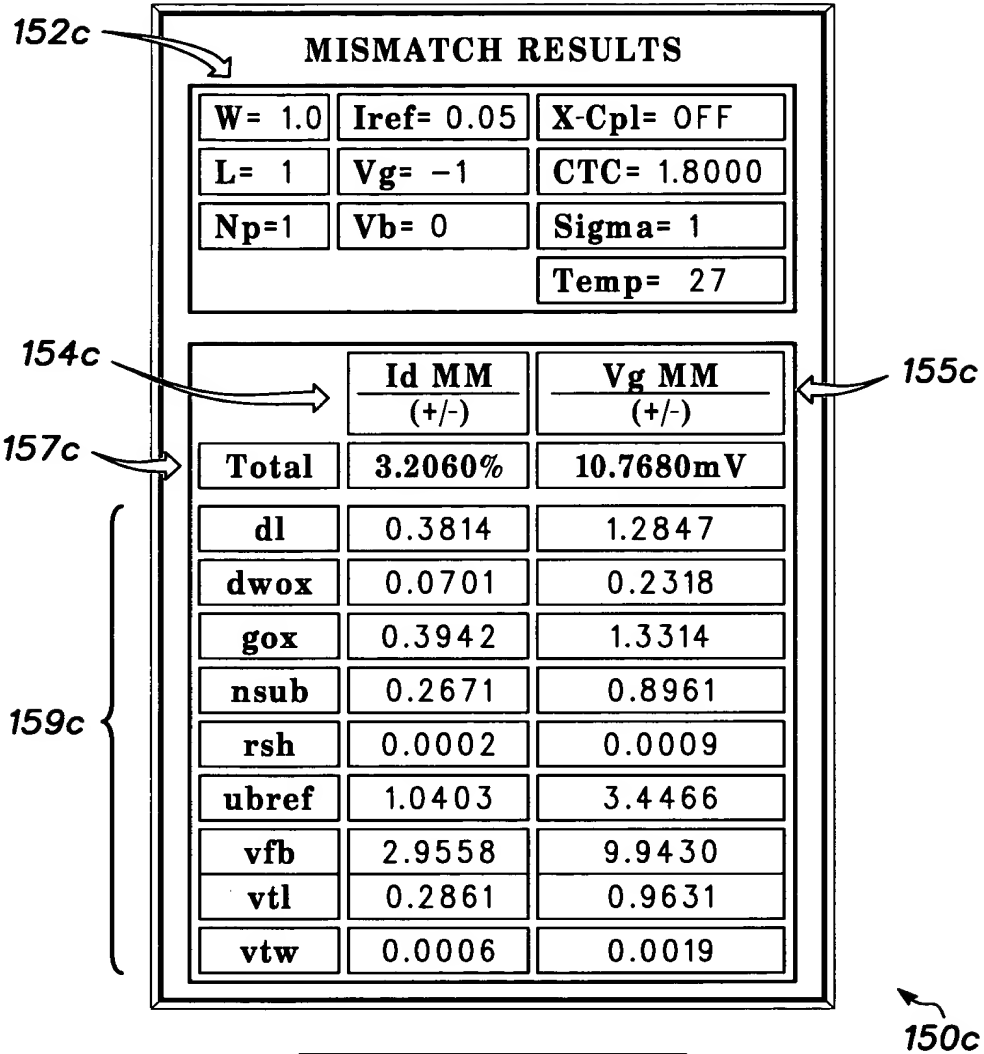


FIG. 6

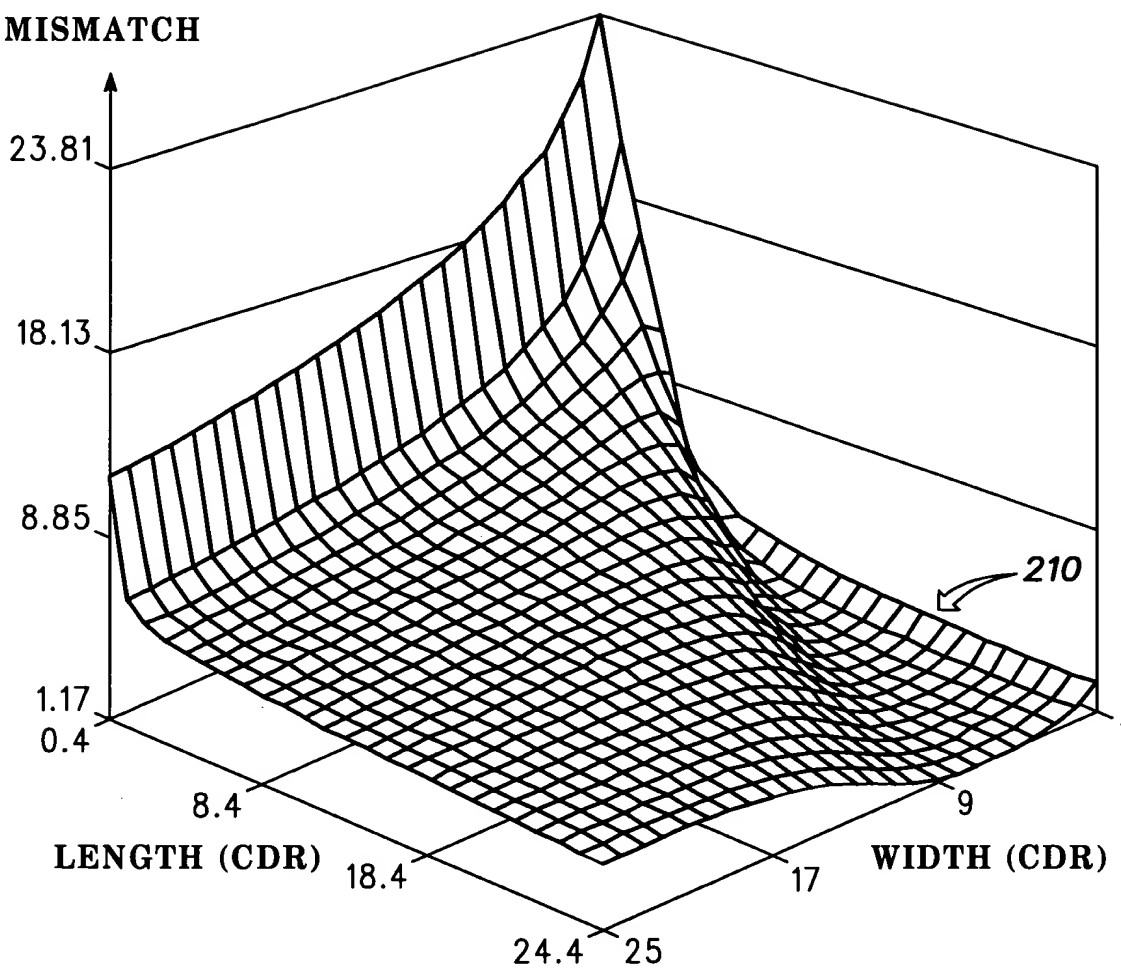
158

001050" 05045960

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

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Id MISMATCH



200

FIG. 7

MISMATCH CALCULATOR DATA ENTRY

☐ cdr1bc

☐ RES

☐ rp2nsd

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MISMATCH RESULTS

Total

Resistor MM (+/-)

2.8892%

Resistor Size = 315.2 ohms

Info

MISMATCH CALCULATOR DATA ENTRY

☐ cdr1bc

☐ RES

☐ rp2nsd

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MISMATCH RESULTS

Total

Resistor MM (+/-)

2.8892%

Resistor Size = 315.2 ohms

Info

MISMATCH CALCULATOR DATA ENTRY

☐ cdr1bc

☐ RES

☐ rp2nsd

[Create Data Entry Form](#)

[View Related Plots](#)

MISMATCH RESULTS

Total

Resistor MM (+/-)

2.8892%

Resistor Size = 315.2 ohms

Info

FIG. 8

